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Control Flow Analysis

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- **Control flow analysis for TIP with first-class functions**
- Control flow analysis for the λ -calculus
- The cubic framework
- Control flow analysis for object-oriented languages



TIP with first-class functions

```
inc(i) { return i+1; }
dec(j) { return j-1; }
ide(k) { return k; }

foo(n,f) {
  var r;
  if (n==0) { f=ide; }
  r = f(n);
  return r;
}

main() {
  var x,y;
  x = input;
  if (x>0) { y = foo(x,inc); } else { y = foo(x,dec); }
  return y;
}
```



Control Flow Complications

- **First-class functions** in TIP complicate CFG construction
 - Several functions may be invoked at a call site
 - This depends on the dataflow
 - But dataflow analysis first requires a CFG

- **Same situation for other features, e.g.**
 - Function values with free variables (**closures**)
 - A class hierarchy with objects and methods
 - Prototype objects with dynamic properties



- **A control flow analysis approximates the call graph**
 - Conservatively computes possible functions at call sites
 - The trivial answer: *all* functions

- **Control flow analysis is usually flow-*insensitive*:**
 - It is based on the AST
 - The call graph can be used for an interprocedural CFG
 - A subsequent dataflow analysis may use the CFG

- **Alternative: use flow-*sensitive* analysis**
 - Potentially *on-the-fly*, during dataflow analysis



- For a computed function call

$$E(E_1, \dots, E_n)$$

we cannot immediately see which function is called

- A coarse but sound approximation

- Assume any function with right number of arguments

- Use CFA to get a much better result



- Tokens are all functions $\{f_1, f_2, \dots, f_k\}$
- For every AST node, v , we introduce the variable $\llbracket v \rrbracket$ denoting the set of functions to which v may evaluate
- For function definitions $f(\dots)\{\dots\}$:
 $f \in \llbracket f \rrbracket$
- For assignments $x = E$:
 $\llbracket E \rrbracket \subseteq \llbracket x \rrbracket$



□ For **direct** function calls $f(E_1, \dots, E_n)$:

$$\llbracket E_i \rrbracket \subseteq \llbracket a_i \rrbracket \text{ for } i = 1, \dots, n \wedge \llbracket E' \rrbracket \subseteq \llbracket f(E_1, \dots, E_n) \rrbracket$$

where f is a function with arguments a_1, \dots, a_n and return expression E'

• For **computed** function calls $E(E_1, \dots, E_n)$:

$$f \in \llbracket E \rrbracket \Rightarrow (\llbracket E_i \rrbracket \subseteq \llbracket a_i \rrbracket \text{ for } i = 1, \dots, n \wedge \llbracket E' \rrbracket \subseteq \llbracket E(E_1, \dots, E_n) \rrbracket)$$

for every function f with arguments a_1, \dots, a_n and return expression E'

■ If consider **typable** programs only:

Only generate constraints for those functions f for which the call would be type correct



Generated Constraints

```
inc ∈ [inc]
dec ∈ [dec]
ide ∈ [ide]
[ide] ⊆ [f]
[f(n)] ⊆ [r]
inc ∈ [f] ⇒ [n] ⊆ [i] ∧ [i+1] ⊆ [f(n)]
dec ∈ [f] ⇒ [n] ⊆ [j] ∧ [j-1] ⊆ [f(n)]
ide ∈ [f] ⇒ [n] ⊆ [k] ∧ [k] ⊆ [f(n)]
[input] ⊆ [x]
[foo(x, inc)] ⊆ [y]
[foo(x, dec)] ⊆ [y]
foo ∈ [foo]
foo ∈ [foo] ⇒ [x] ⊆ [n] ∧ [inc] ⊆ [f] ∧ [r] ⊆ [foo(x, inc)]
foo ∈ [foo] ⇒ [x] ⊆ [n] ∧ [dec] ⊆ [f] ∧ [r] ⊆ [foo(x, dec)]
main ∈ [main]
```

```
inc(i) { return i+1; }
dec(j) { return j-1; }
ide(k) { return k; }
```

```
foo(n, f) {
  var r;
  if (n==0) { f=ide; }
  r = f(n);
  return r;
}
```

```
main() {
  var x, y;
  x = input;
  if (x>0) { y = foo(x, inc); } else { y = foo(x, dec); }
  return y;
}
```

} assuming we do not
use the special rule
for direct calls

(At each call we only consider functions with matching number of parameters)



```
[[inc]] = {inc}
[[dec]] = {dec}
[[ide]] = {ide}
[[f]] = {inc, dec, ide}
[[foo]] = {foo}
[[main]] = {main}
```

(the solution is the empty set for the remaining constraint variables)

With this information, we can construct the call edges and return edges in the interprocedural CFG



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□ The pure lambda calculus

$E \rightarrow \lambda x.E$	(function definition)
$E_1 E_2$	(function application)
x	(variable reference)

- Assume all λ -bound variables are distinct
- An **abstract closure** λx abstracts the function $\lambda x.E$ in all contexts (values of free variables)
- Goal: for each call site $E_1 E_2$ determine the possible functions for E_1 from the set $\{\lambda x_1, \lambda x_2, \dots, \lambda x_n\}$



A flow-**in**sensitive analysis that tracks function values:

- For every AST node, v , we introduce a variable $\llbracket v \rrbracket$ ranging over **subsets of abstract closures**
- For $\lambda x.E$ we have the constraint

$$\lambda x \in \llbracket \lambda x.E \rrbracket$$

- For $E_1 E_2$ we have the *conditional* constraint

$$\lambda x \in \llbracket E_1 \rrbracket \Rightarrow (\llbracket E_2 \rrbracket \subseteq \llbracket x \rrbracket \wedge \llbracket E \rrbracket \subseteq \llbracket E_1 E_2 \rrbracket)$$

for every function $\lambda x.E$



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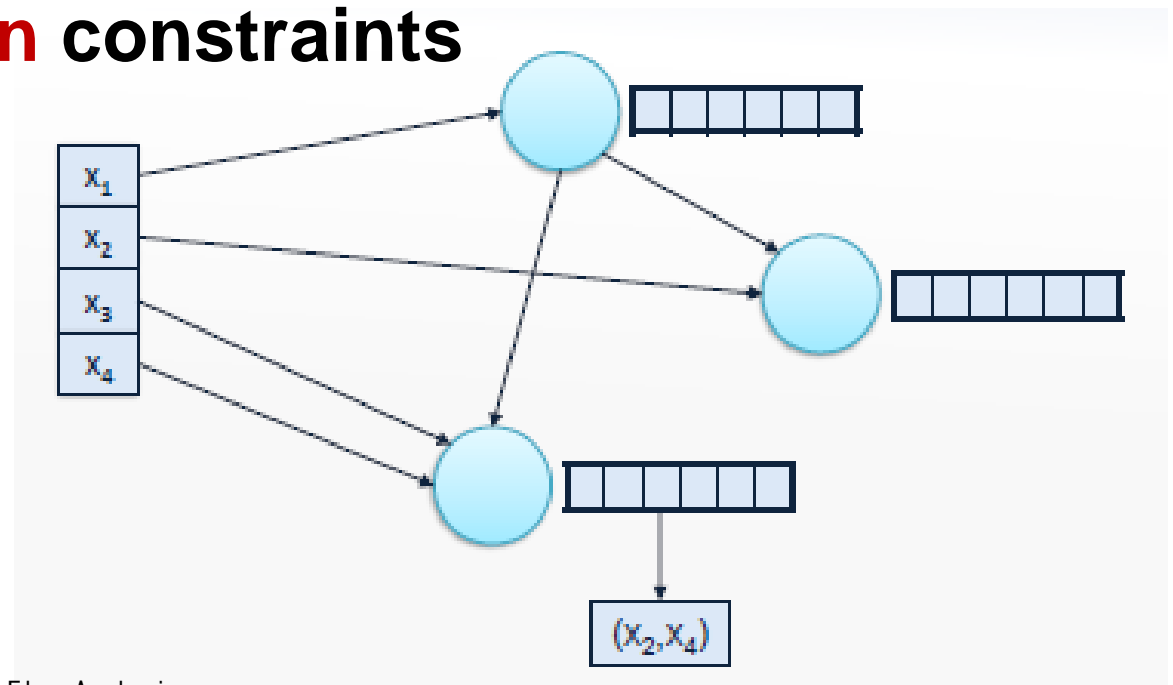
- We have a set of **tokens** $\{t_1, t_2, \dots, t_k\}$
- We have a collection of **variables** $\{x_1, \dots, x_n\}$ whose values range over subsets of tokens
- A collection of constraints of these forms:

- $t \in x$
- $t \in x \Rightarrow y \subseteq z$

- **Compute the unique minimal solution**
 - This exists since solutions are closed under intersection
- **A cubic time algorithm exists!**

- Each variable is mapped to a node in a DAG
- Each node has a bitvector in $\{0,1\}^k$
 - initially set to all 0's
- Each **bit** has a list of pairs of variables
 - used to model **conditional constraints**
- The DAG **edges** model **inclusion constraints**
- The bitvectors will at all times directly represent the minimal solution to the constraints seen so far

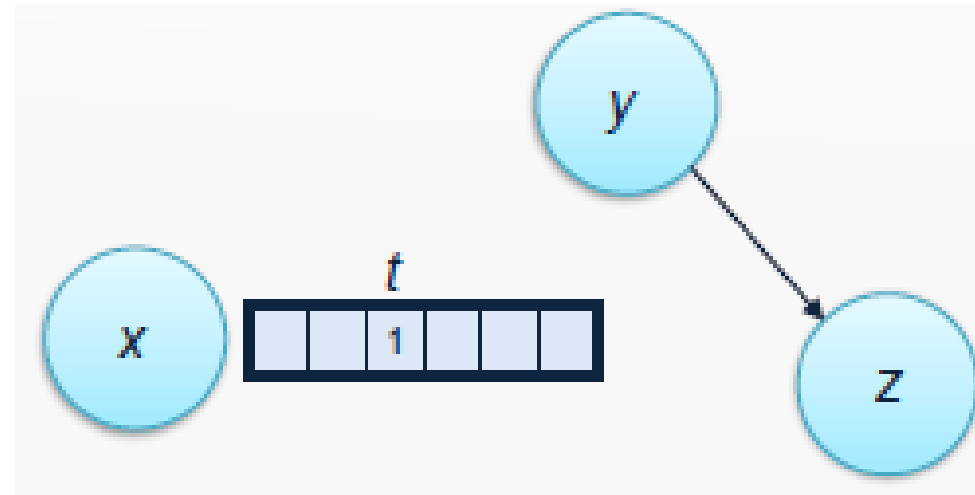
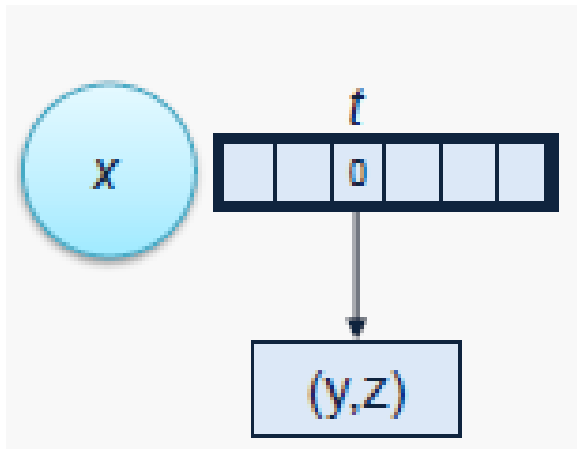
```
■  $t \in x$   
■  $t \in x \Rightarrow y \subseteq z$ 
```



□ Constraints of the form $t \in x$:

- look up the node associated with x
- set the bit corresponding to t to 1
- if the list of pairs for t is not empty, then add the edges corresponding to the pairs to the DAG

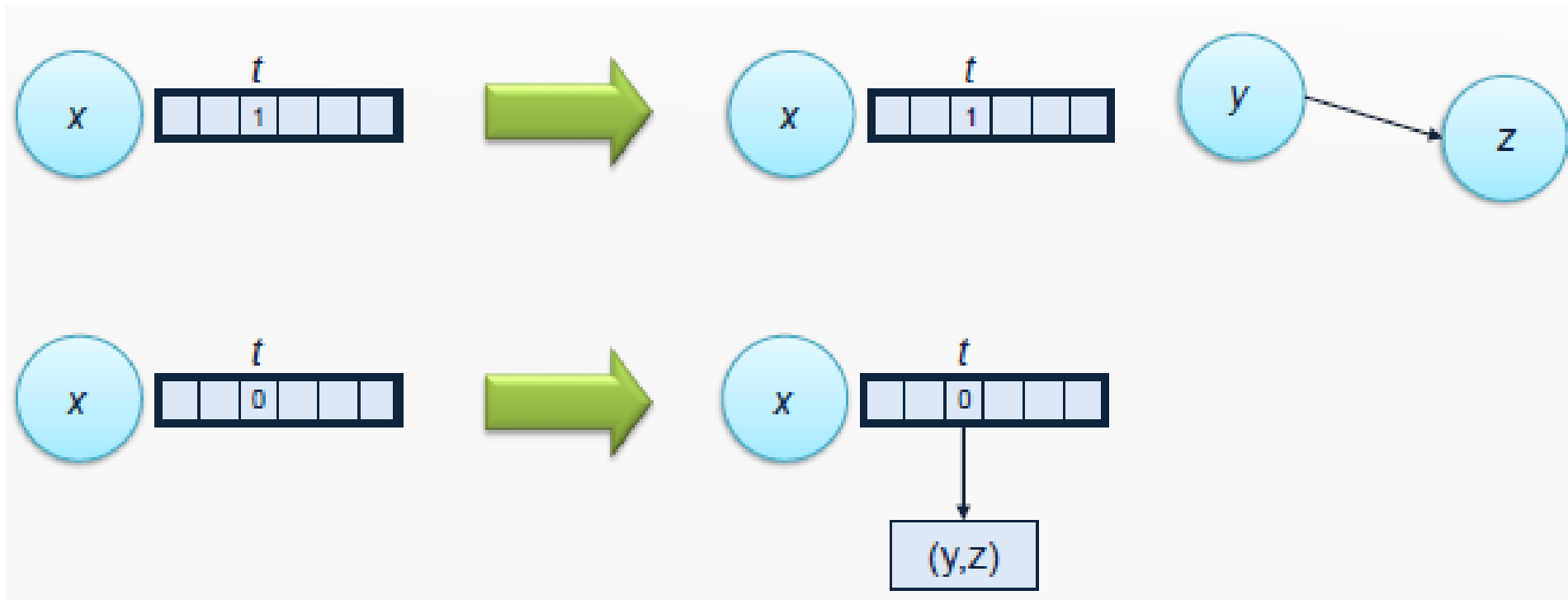
- $t \in x$
- $t \in x \Rightarrow y \subseteq z$



□ Constraints of the form $t \in X \Rightarrow Y \subseteq Z$:

- test if the bit corresponding to t is 1
- if so, add the DAG edge from y to z
- otherwise, add (y,z) to the list of pairs for t

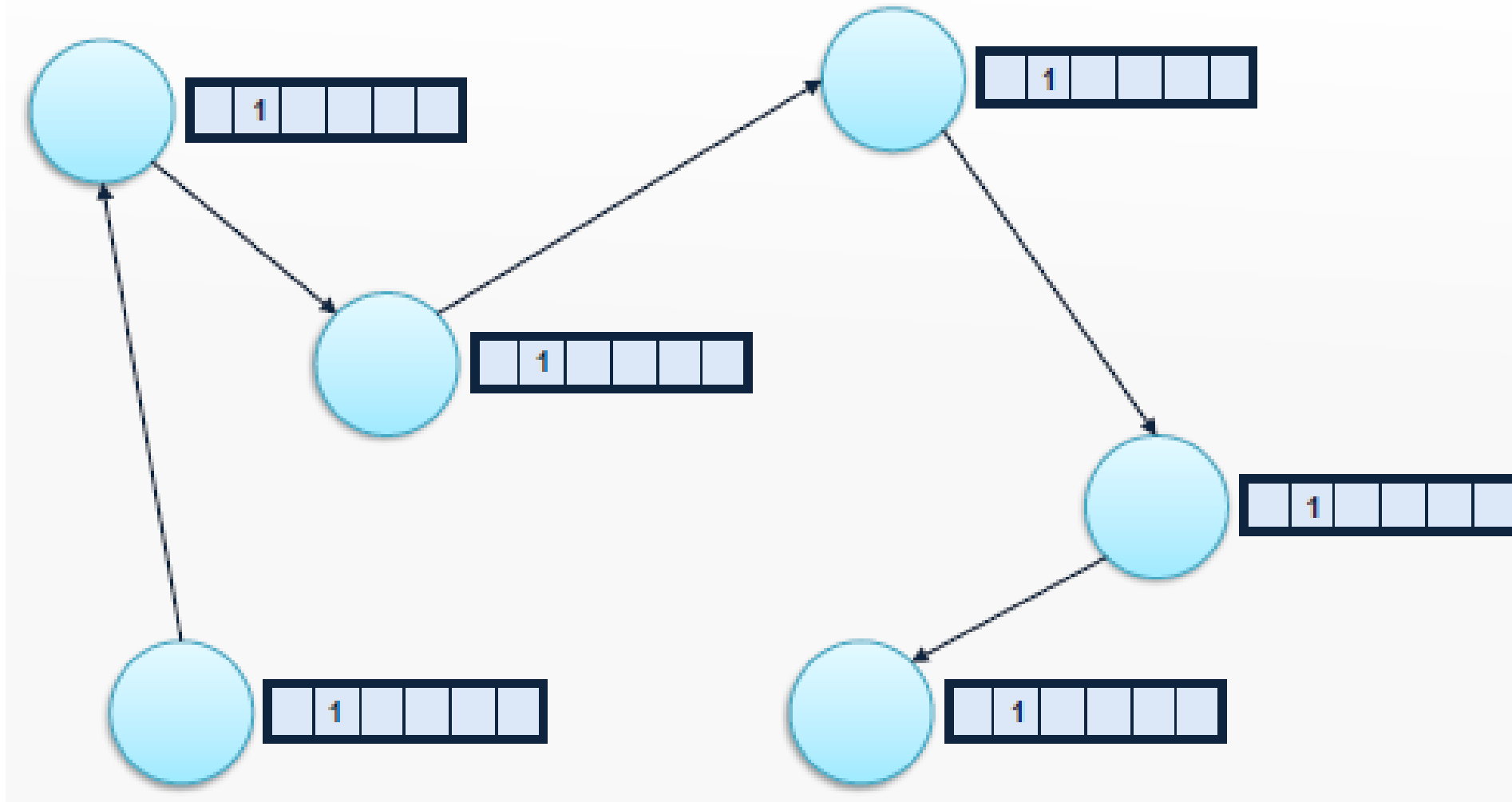
- $t \in X$
- $t \in X \Rightarrow Y \subseteq Z$





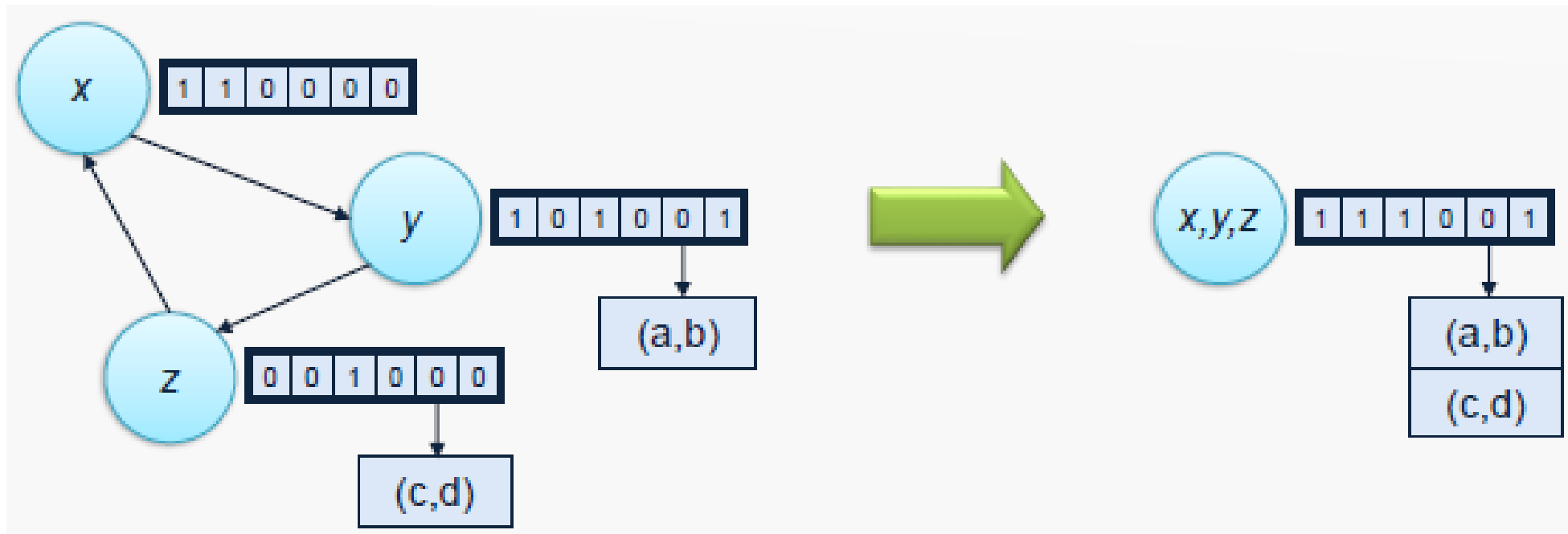
Propagate Bitvectors

- Propagate the values of all newly set bits along all edges in the DAG



□ If a newly added edge forms a cycle:

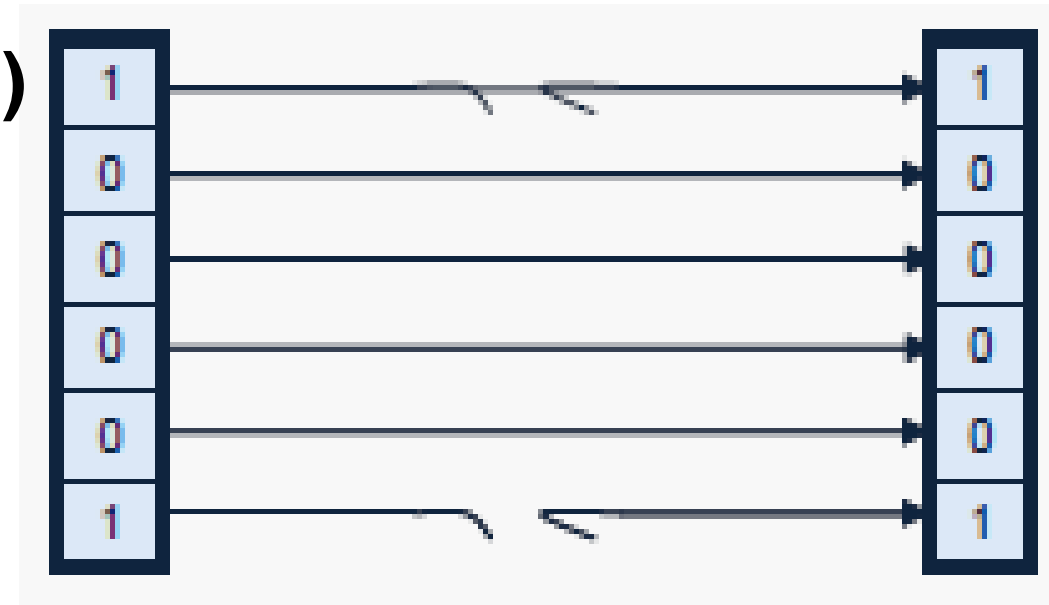
- merge the nodes on the cycle into a single node
- form the union of the bitvectors
- concatenate the lists of pairs
- update the map from variables accordingly





Time Complexity(1/2)

- $O(n)$ functions and $O(n)$ applications, with program size n
- $O(n)$ singleton constraints, $O(n^2)$ conditional constraints
- $O(n)$ nodes, $O(n^2)$ edges, $O(n)$ bits per node
- Total time for bitvector propagation: $O(n^3)$
- Total time for collapsing cycles: $O(n^3)$
- Total time for handling lists of pairs: $O(n^3)$





Time Complexity(1/2)

- Adding it all up, the upper bound is $O(n^3)$

- This is known as the *cubic time bottleneck*:
 - Occurs in many different scenarios
 - but $O(n^3/\log n)$ is possible...

- A special case of general set constraints:
 - Defined on sets of *terms* instead of sets of tokens
 - solvable in time $O(2^{2^n})$



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Simple CFA for OO (1/3)

- CFA in an object-oriented language:

```
x.m(a, b, c)
```

- Which method implementations may be invoked?
- Full CFA is a possibility...
- But the extra structure allows simpler solutions



Simple CFA for OO (2/3)

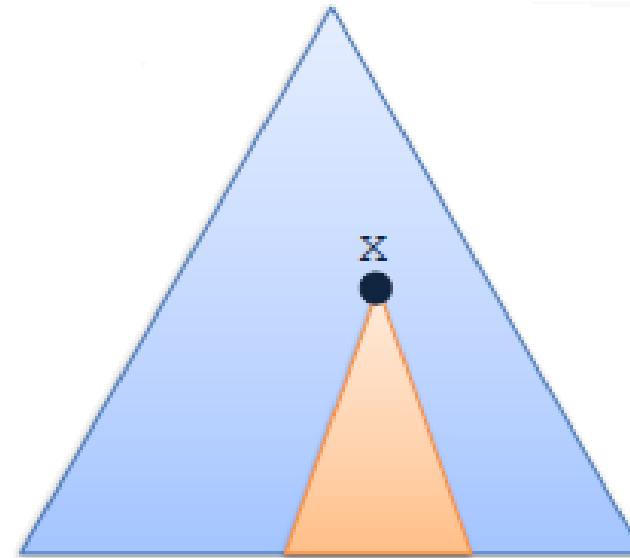
□ Simplest solution:

- Select all methods named m with three arguments

□ Class Hierarchy Analysis(CHA):

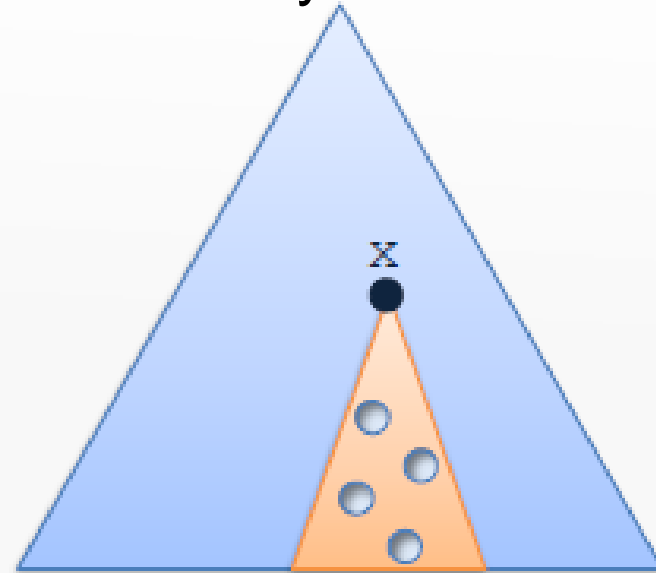
- Consider only the part of the class hierarchy rooted by the declared type of x

```
collection<T> c | = ...  
c.add(e)
```



□ Rapid Type Analysis (RTA):

- Restrict to those classes that are actually used in the program in **new** expressions
- Start from **main**, iteratively find reachable methods



□ Variable Type Analysis (VTA):

- perform *intraprocedural* control flow analysis



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Thanks